

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gajadharsing et al.
App. No : 10/551,324
Filed : October 18, 2005
For : SEMICONDUCTOR TRANSISTOR
(DMOS) DEVICE FOR USE AS A
POWER AMPLIFIER
Examiner : Sarah Kate Salerno
Art Unit : 2814
Conf No. : 7460

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(Date)



Christian A. Fox, Reg. No. 58,507

RESPONSE TO NOVEMBER 13, 2008 ADVISORY ACTION AND
AUGUST 21, 2008 FINAL OFFICE ACTION

Mail Stop AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Advisory Action mailed November 13, 2008, and further to the Response to August 21, 2008 Final Office Action filed October 21, 2008, Applicant respectfully submits the following:

A **Listing of Claims** begins on page 2 of this paper (no claim amendments are being presented).

Remarks begin on page 5 of this paper.